09/751408

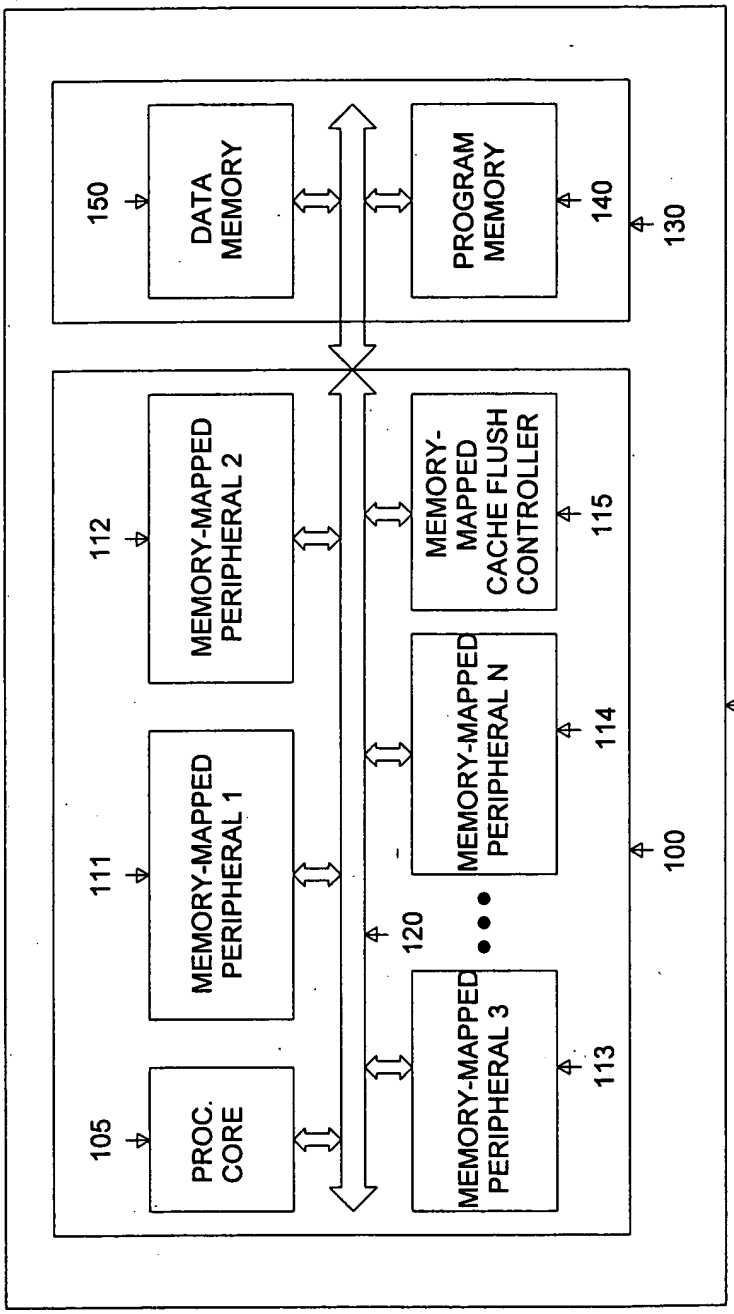

FIGURE 1

09/751408

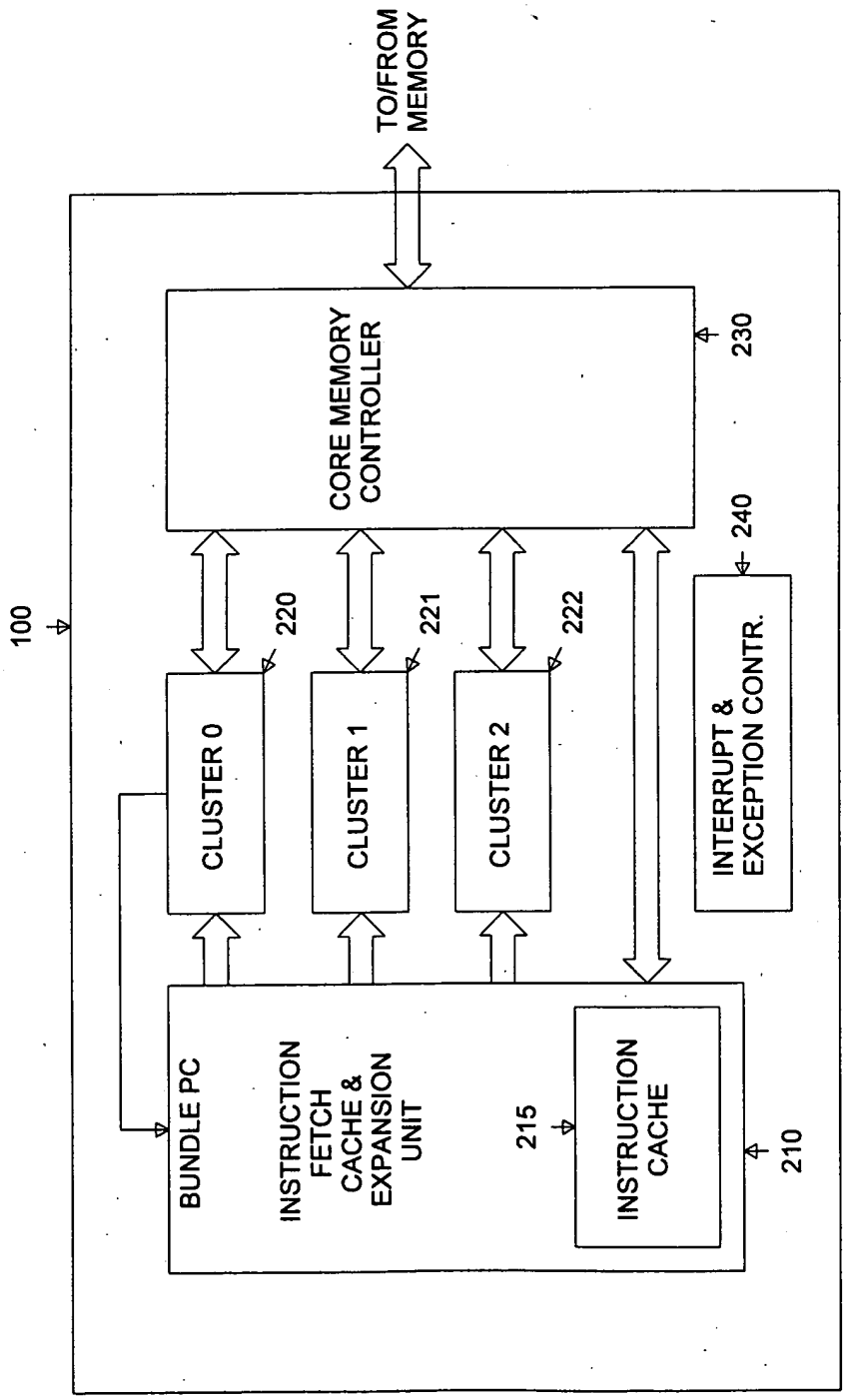

FIGURE 2

09/751408

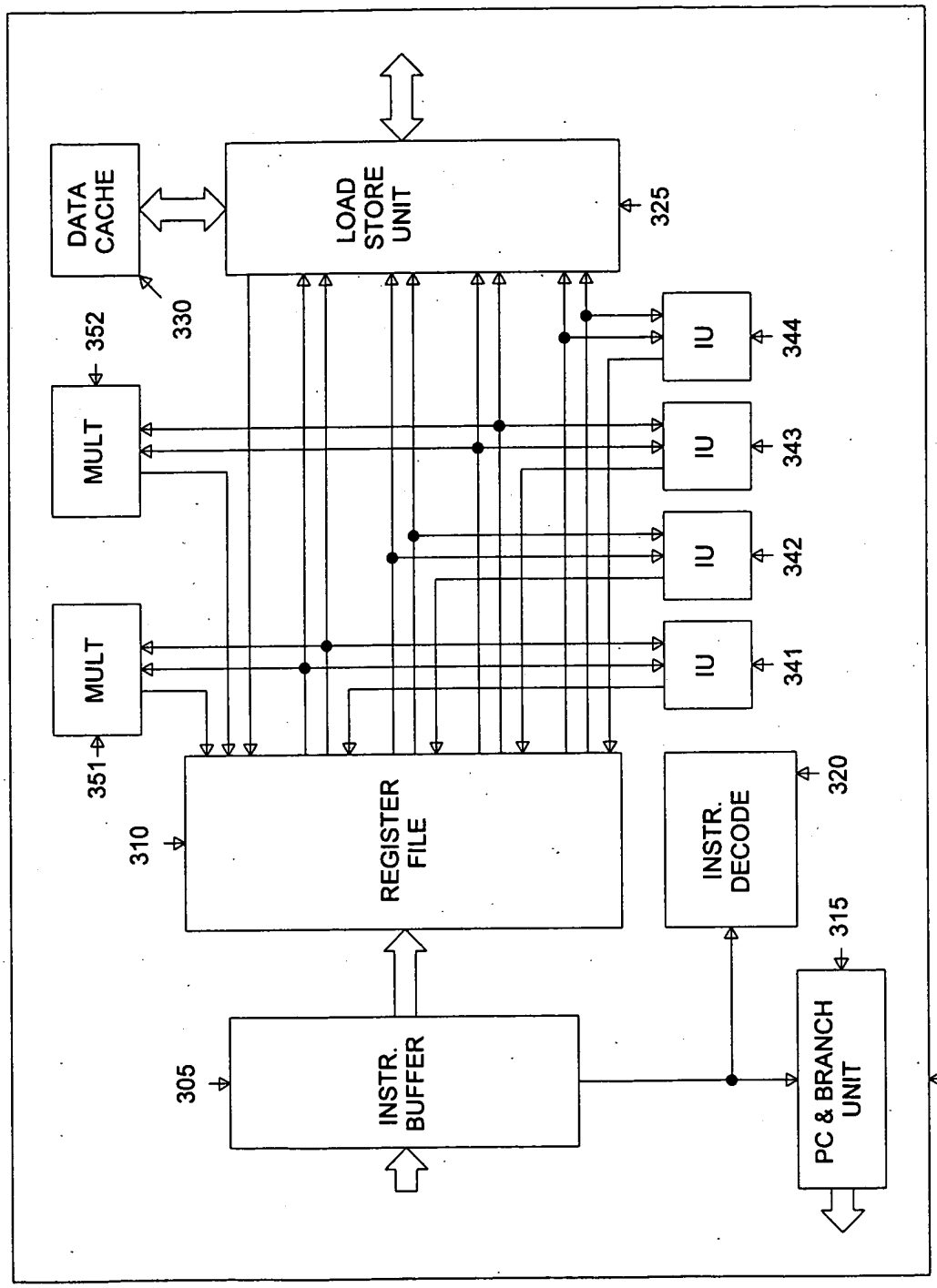

FIGURE 3

09/751408

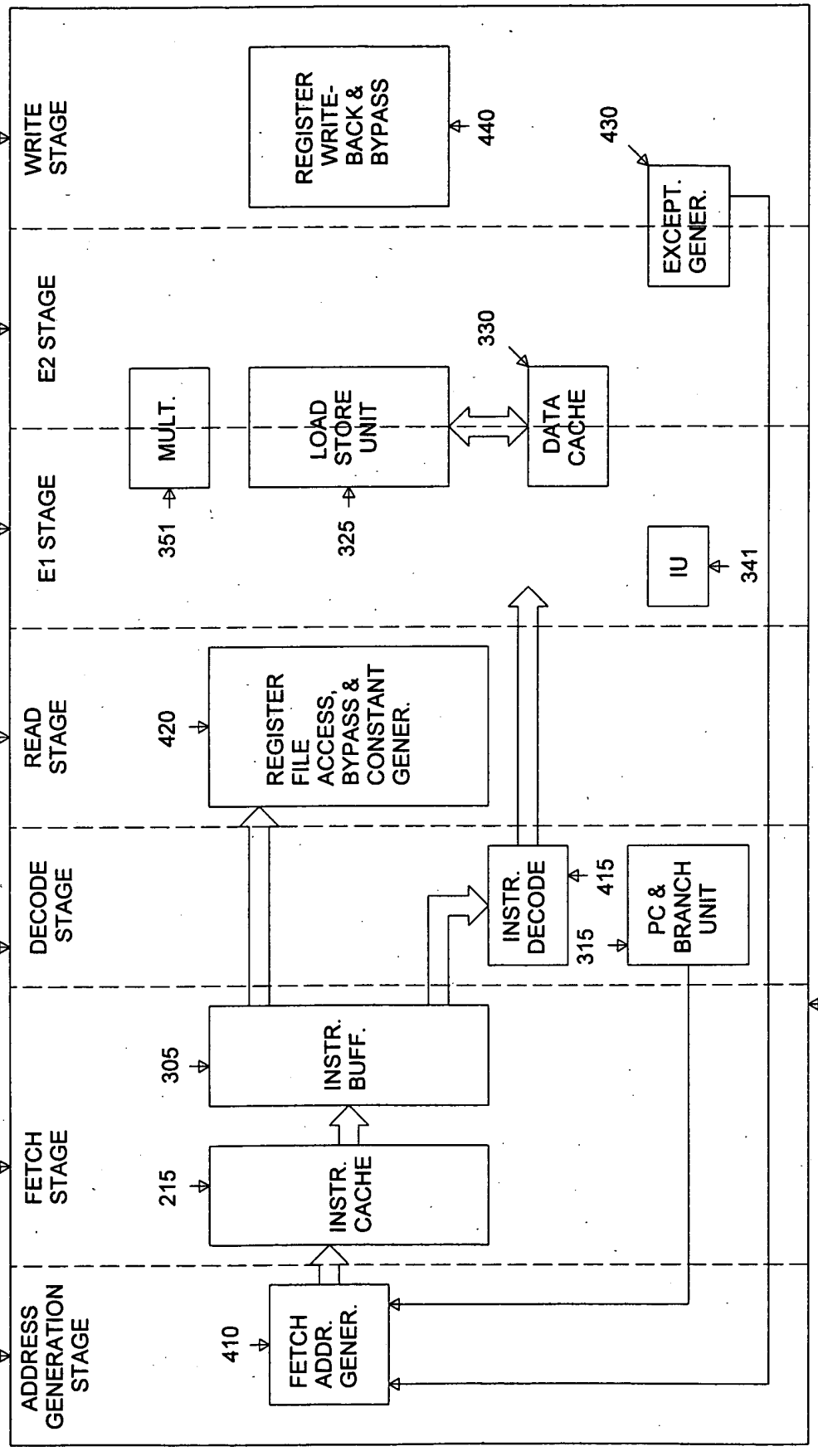

FIGURE 4

09/751408

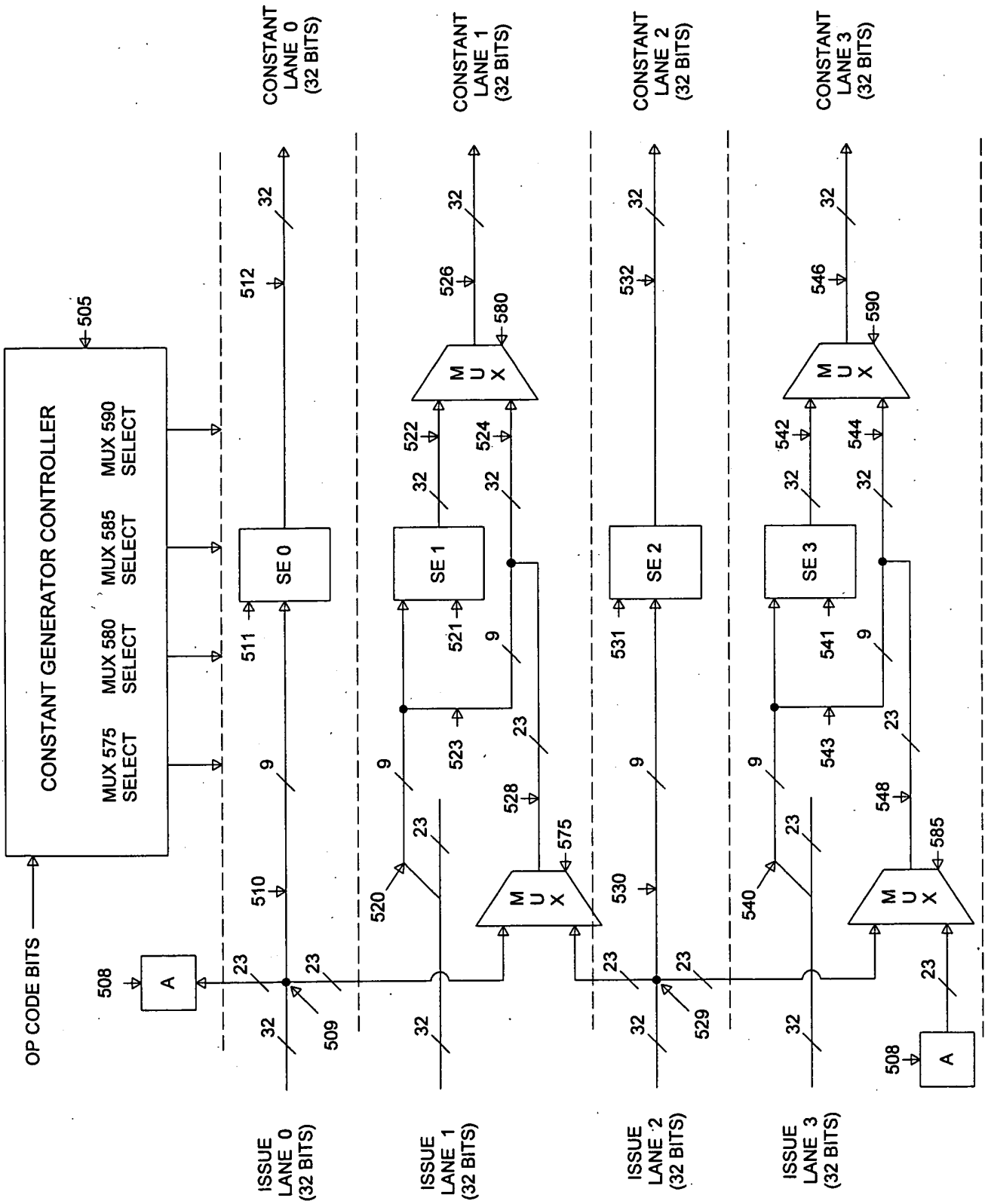

FIGURE 5

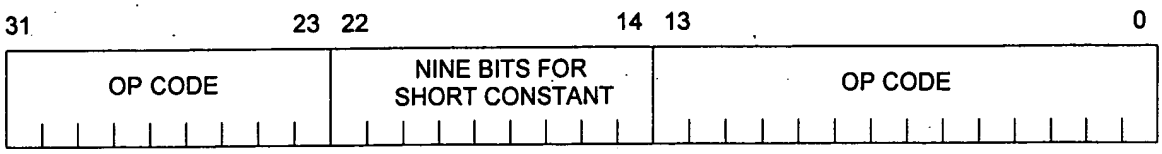

FIGURE 6

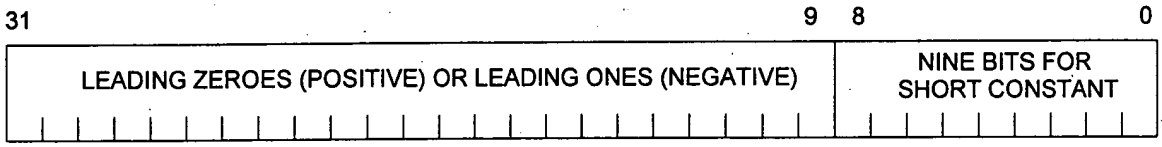

FIGURE 7

Docket #: 00-BN-057

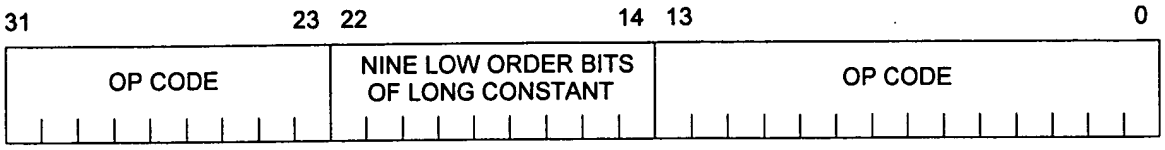

FIGURE 8

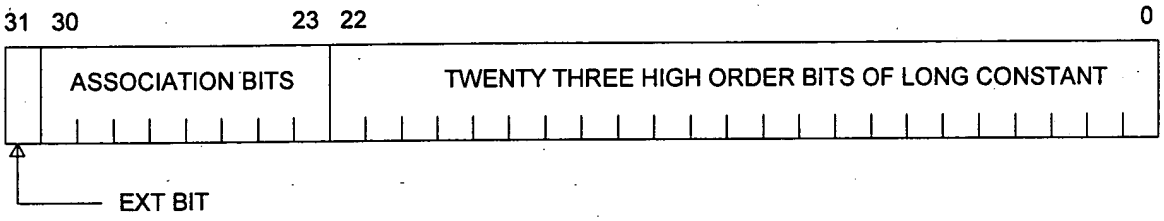

FIGURE 9

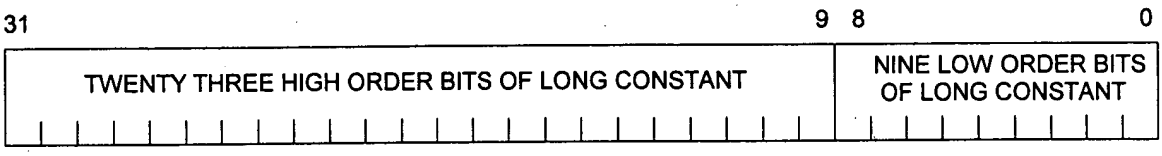

FIGURE 10

09351908 - 112000

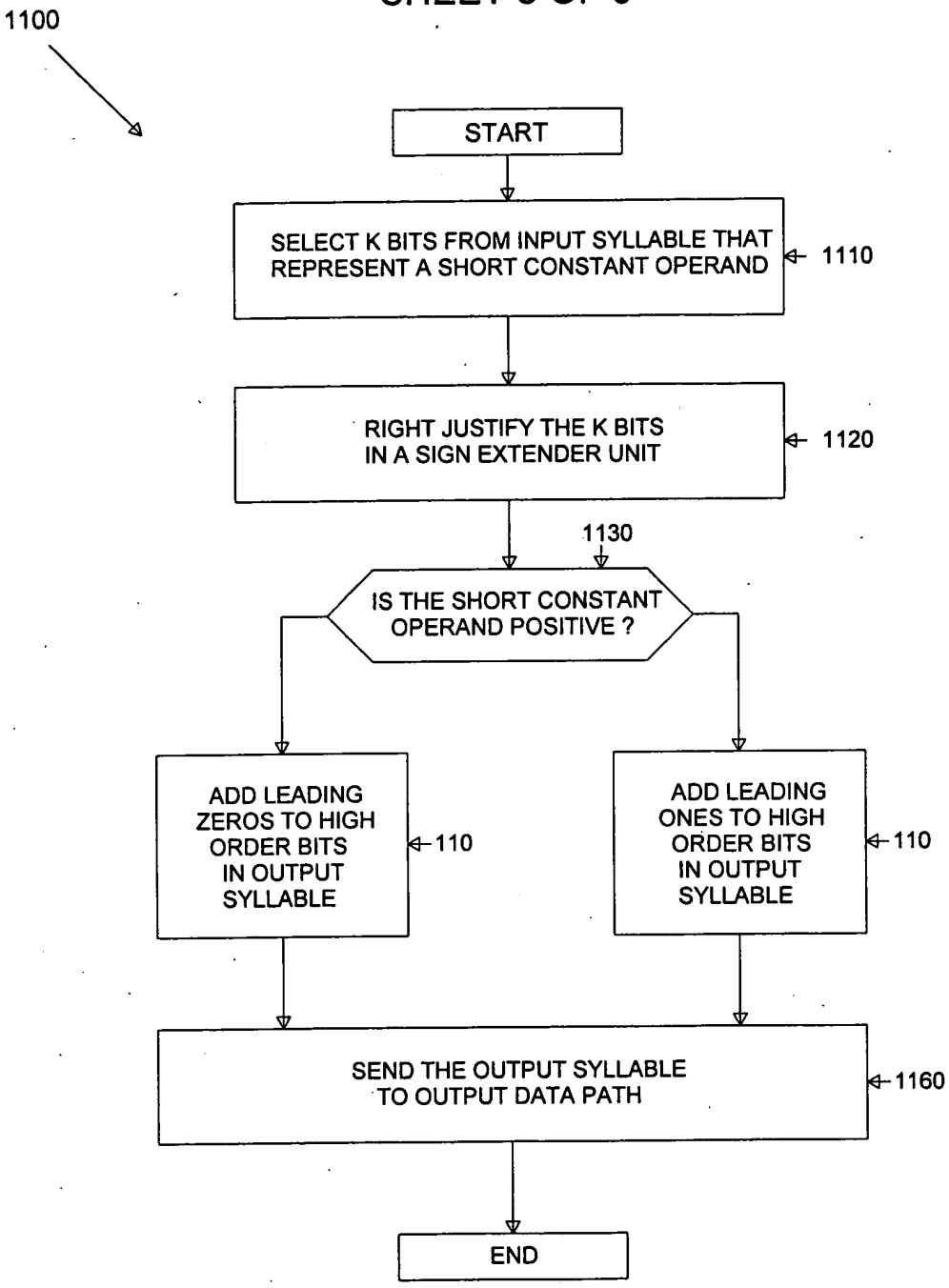
1100
START
SELECT K BITS FROM INPUT SYLLABLE THAT REPRESENT A SHORT CONSTANT OPERAND
1110
RIGHT JUSTIFY THE K BITS IN A SIGN EXTENDER UNIT
1120
1130
IS THE SHORT CONSTANT OPERAND POSITIVE ?
ADD LEADING ZEROS TO HIGH ORDER BITS IN OUTPUT SYLLABLE
110
ADD LEADING ONES TO HIGH ORDER BITS IN OUTPUT SYLLABLE
110
SEND THE OUTPUT SYLLABLE TO OUTPUT DATA PATH
1160
END

FIGURE 11

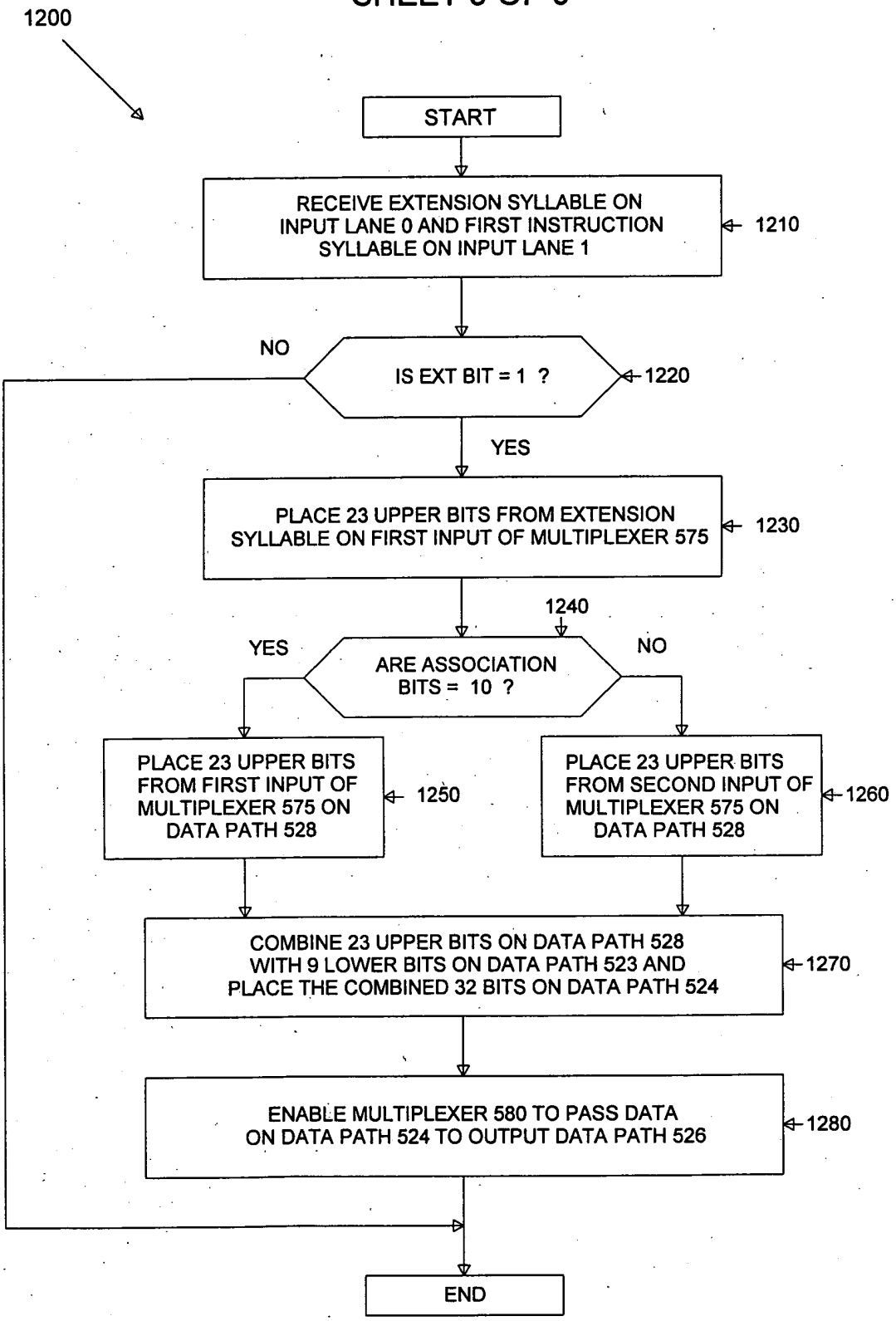
1200
START
RECEIVE EXTENSION SYLLABLE ON INPUT LANE 0 AND FIRST INSTRUCTION SYLLABLE ON INPUT LANE 1
1210
NO
IS EXT BIT = 1 ?
1220
YES
PLACE 23 UPPER BITS FROM EXTENSION SYLLABLE ON FIRST INPUT OF MULTIPLEXER 575
1230
1240
YES
ARE ASSOCIATION BITS = 10 ?
NO
PLACE 23 UPPER BITS FROM FIRST INPUT OF MULTIPLEXER 575 ON DATA PATH 528
1250
PLACE 23 UPPER BITS FROM SECOND INPUT OF MULTIPLEXER 575 ON DATA PATH 528
1260
COMBINE 23 UPPER BITS ON DATA PATH 528 WITH 9 LOWER BITS ON DATA PATH 523 AND PLACE THE COMBINED 32 BITS ON DATA PATH 524
1270
ENABLE MULTIPLEXER 580 TO PASS DATA ON DATA PATH 524 TO OUTPUT DATA PATH 526
1280
END

FIGURE 12